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Biography:

Dr. H.-S. Philip Wong currently serves as Vice President of Corporate Research responsible for exploring new technologies for TSMC. Prior to joining TSMC, Dr. Wong is Professor of Electrical Engineering and holds the Willard R. and Inez Kerr Bell Professorship in the School of Engineering at Stanford University. He has 16 years of research and path-finding experience at IBM Research, where many of his early research works have led to product technologies. His research aims at translating discoveries in science into practical technologies and has contributed to advancements in nanoscale science and technology, semiconductor technology, solid-state devices, and electronic imaging. He is the founding Faculty Co-Director of the Stanford SystemX Alliance and Faculty Director of the Stanford Non-Volatile Memory Technology Research Initiative (NMTRI). He has held leadership positions at major multi-university research centers of the National Science Foundation and the Semiconductor Research Corporation.

Dr. Wong received his B.Sc. (Hons.) from the University of Hong Kong, his M.S. degree from Stony Brook University, and his Ph.D. from Lehigh University, all in electrical engineering. He has published over 600 papers at international conferences and technical journals and is the co-author of a book and 11 book chapters. He holds 52 US patents. He is an IEEE Fellow. He received the honorary doctorate degree from Institut Polytechnique de Grenoble, France. Dr. Wong and his students have won best paper awards at premier conferences such as the International Solid-State Circuits Conference (ISSCC) and Symposia on VLSI. He served as General Chair of the International Electron Devices Meeting (IEDM) and is currently the Chair of the IEEE Executive Committee of the Symposia on VLSI Technology and Circuits.